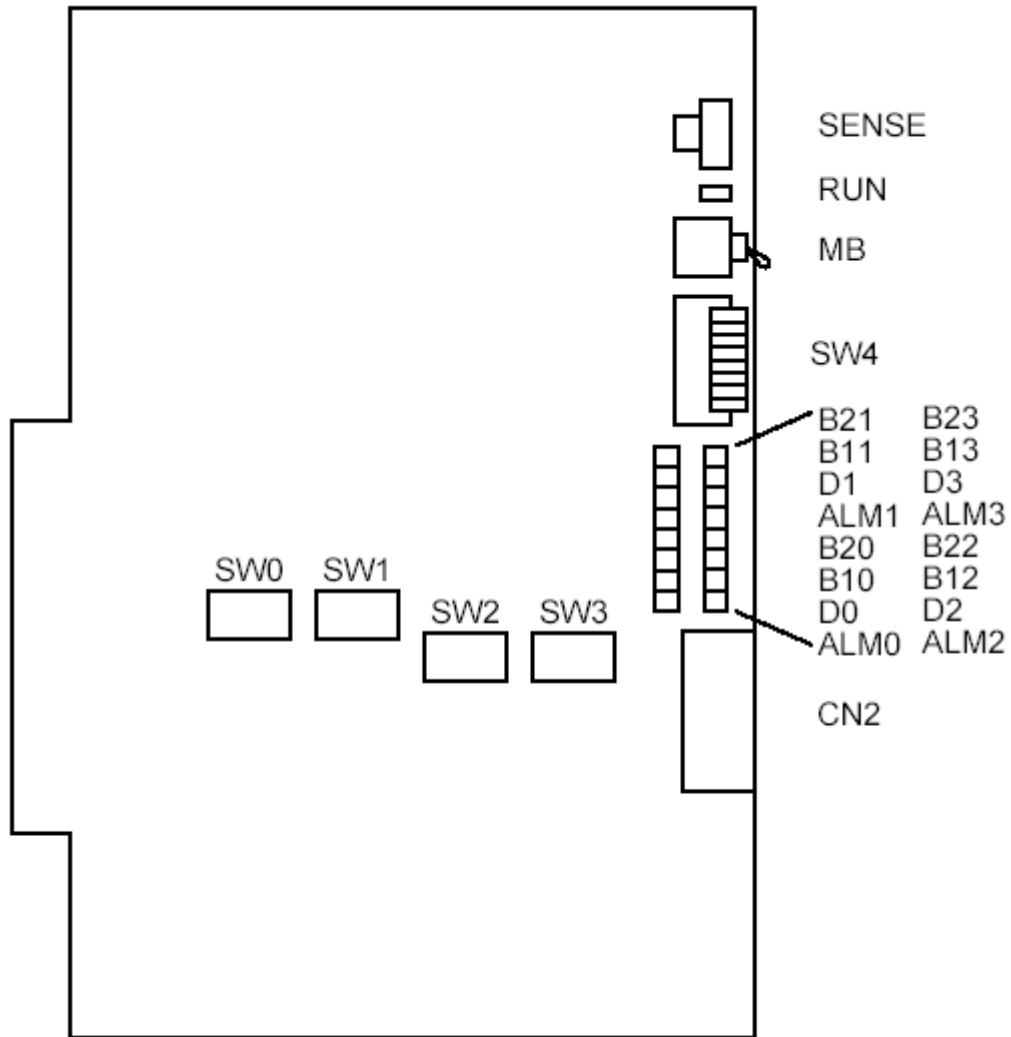


Technology Group		Engineering Release Notice
PROGRAM/PACKAGE NAME: PN-4BRТА-A		RELEASE DATE: 11/11/2005
SYSTEM TYPE:		IPS/IPS Retrofit/IPS DM
APPLICATION:		ETSI ISDN BRA ONLY
REASON FOR RELEASE:		
<input type="checkbox"/>	Fault Rectification	<input type="checkbox"/>
	Enhancements	<input type="checkbox"/>
		Information Attached
<input checked="" type="checkbox"/>	Other: Card Replacement	
SOFTWARE/HARDWARE SUPERSEDED BY THIS ISSUE: PN-2BRT		
MINIMUM FIRMWARE REQUIRED: SC-3250 ISSUE A2		
MINIMUM HARDWARE REQUIRED: 2A		
MINIMUM MAT SOFTWARE REQUIRED: N/A		
DOCUMENTATION REQUIRED: NEC-6809 (IPS ISDN SYSTEM MANUAL)		
GENERAL INFORMATION: IPS MAIN PROGRAM R9 (J1 Version 6.03) minimum		
Author: Steve Morris		Doc ID: NEC-9274



CARD LAYOUT

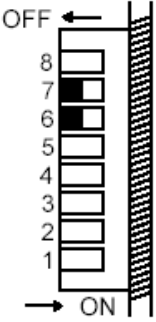


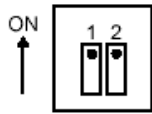
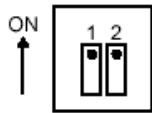
Lamp Indications

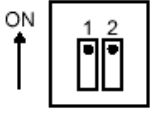
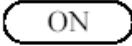
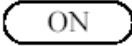
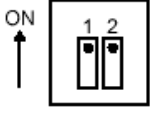


LAMP NAME	COLOR	FUNCTION	
RUN	Green	Flashes at 120 IPM while this card is operating normally.	
B23	Red	No.3 Circuit	B2 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
B13	Red		B1 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
D3	Green		D channel status ON: Link is connected OFF: Link is not connected
ALM3	Red		Transmission line fault status ON: Line fault OFF: Normal operation
B22	Red	No.2 Circuit	B2 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
B12	Red		B1 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
D2	Green		D channel status ON: Link is connected OFF: Link is not connected
ALM2	Red		Transmission line fault status ON: Line fault OFF: Normal operation



LAMP NAME	COLOR	FUNCTION	
B21	Red	No.1 Circuit	B2 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
B11	Red		B1 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
D1	Green		D channel status ON: Link is connected OFF: Link is not connected
ALM1	Red		Transmission line fault status ON: Line fault OFF: Normal operation
B20	Red	No.0 Circuit	B2 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
B10	Red		B1 channel status ON: Busy OFF: Idle Flash (60 IPM): Make Busy
D0	Green		D channel status ON: Link is connected OFF: Link is not connected
ALM0	Red		Transmission line fault status ON: Line fault OFF: Normal operation

SWITCH NAME	SWITCH NUMBER	SETTING POSITION	FUNCTION	CHECK																																										
SENSE (Rotary SW)  NOTE 1	4-F	Set the switch to match the AP Number (04-31) to be set by CM05.	<table border="1"> <tr> <td>AP No.</td> <td>SW4-8: ON</td> <td>04</td><td>05</td><td>06</td><td>07</td><td>08</td><td>09</td><td>10</td><td>11</td><td>12</td><td>13</td><td>14</td><td>15</td> </tr> <tr> <td></td> <td>SW4-8: OFF</td> <td>20</td><td>21</td><td>22</td><td>23</td><td>24</td><td>25</td><td>26</td><td>27</td><td>28</td><td>29</td><td>30</td><td>31</td> </tr> <tr> <td></td> <td>SW No.</td> <td>4</td><td>5</td><td>6</td><td>7</td><td>8</td><td>9</td><td>A</td><td>B</td><td>C</td><td>D</td><td>E</td><td>F</td> </tr> </table>	AP No.	SW4-8: ON	04	05	06	07	08	09	10	11	12	13	14	15		SW4-8: OFF	20	21	22	23	24	25	26	27	28	29	30	31		SW No.	4	5	6	7	8	9	A	B	C	D	E	F	
	AP No.	SW4-8: ON	04	05	06	07	08	09	10	11	12	13	14	15																																
	SW4-8: OFF	20	21	22	23	24	25	26	27	28	29	30	31																																	
	SW No.	4	5	6	7	8	9	A	B	C	D	E	F																																	
0-3	Not used																																													
MB (Toggle SW)  NOTE 2	/	UP	For make-busy																																											
		DOWN	For normal operation																																											

SWITCH NAME	SWITCH NUMBER	SETTING POSITION	FUNCTION	CHECK																																																																																																																																														
SW4 (Piano SW) 	Output clock signals to PLO0/PLO1 of MP card. NOTE 3																																																																																																																																																	
	<table border="1"> <thead> <tr> <th colspan="4">SW No.</th> <th colspan="4">Circuit No.</th> </tr> <tr> <th>1</th> <th>2</th> <th>3</th> <th>4</th> <th>No.0</th> <th>No.1</th> <th>No.2</th> <th>No.3</th> </tr> </thead> <tbody> <tr><td>OFF</td><td>OFF</td><td>OFF</td><td>OFF</td><td>-</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>ON</td><td>OFF</td><td>OFF</td><td>OFF</td><td>PLO0</td><td>-</td><td>-</td><td>-</td></tr> <tr><td>OFF</td><td>ON</td><td>OFF</td><td>OFF</td><td>-</td><td>PLO0</td><td>-</td><td>-</td></tr> <tr><td>ON</td><td>ON</td><td>OFF</td><td>OFF</td><td>PLO0</td><td>PLO1</td><td>-</td><td>-</td></tr> <tr><td>OFF</td><td>OFF</td><td>ON</td><td>OFF</td><td>-</td><td>-</td><td>PLO0</td><td>-</td></tr> <tr><td>OFF</td><td>OFF</td><td>OFF</td><td>ON</td><td>-</td><td>-</td><td>-</td><td>PLO0</td></tr> <tr><td>OFF</td><td>OFF</td><td>ON</td><td>ON</td><td>-</td><td>-</td><td>PLO0</td><td>PLO1</td></tr> <tr><td>ON</td><td>OFF</td><td>ON</td><td>OFF</td><td>PLO0</td><td>-</td><td>PLO1</td><td>-</td></tr> <tr><td>ON</td><td>OFF</td><td>OFF</td><td>ON</td><td>PLO0</td><td>-</td><td>-</td><td>PLO1</td></tr> <tr><td>OFF</td><td>ON</td><td>OFF</td><td>ON</td><td>-</td><td>PLO0</td><td>-</td><td>PLO1</td></tr> <tr><td>OFF</td><td>ON</td><td>ON</td><td>OFF</td><td>-</td><td>PLO0</td><td>PLO1</td><td>-</td></tr> <tr><td>ON</td><td>ON</td><td>ON</td><td>OFF</td><td colspan="4" rowspan="4">Not allowed</td><td colspan="2"></td></tr> <tr><td>ON</td><td>ON</td><td>OFF</td><td>ON</td><td colspan="2"></td></tr> <tr><td>ON</td><td>OFF</td><td>ON</td><td>ON</td><td colspan="2"></td></tr> <tr><td>OFF</td><td>ON</td><td>ON</td><td>ON</td><td colspan="2"></td></tr> <tr><td>ON</td><td>ON</td><td>ON</td><td>ON</td><td colspan="4"></td><td colspan="2"></td></tr> </tbody> </table>				SW No.				Circuit No.				1	2	3	4	No.0	No.1	No.2	No.3	OFF	OFF	OFF	OFF	-	-	-	-	ON	OFF	OFF	OFF	PLO0	-	-	-	OFF	ON	OFF	OFF	-	PLO0	-	-	ON	ON	OFF	OFF	PLO0	PLO1	-	-	OFF	OFF	ON	OFF	-	-	PLO0	-	OFF	OFF	OFF	ON	-	-	-	PLO0	OFF	OFF	ON	ON	-	-	PLO0	PLO1	ON	OFF	ON	OFF	PLO0	-	PLO1	-	ON	OFF	OFF	ON	PLO0	-	-	PLO1	OFF	ON	OFF	ON	-	PLO0	-	PLO1	OFF	ON	ON	OFF	-	PLO0	PLO1	-	ON	ON	ON	OFF	Not allowed						ON	ON	OFF	ON			ON	OFF	ON	ON			OFF	ON	ON	ON			ON	ON	ON	ON						
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OFF	ON	ON	ON																																																																																																																																															
ON	ON	ON	ON																																																																																																																																															
5	ON	Output clock signals according to the switch setting of SW4-1 to SW4-4.																																																																																																																																																
NOTE 3	OFF	Output clock signals to the reverse PLO route of the switch setting of SW4-1 to SW4-4.																																																																																																																																																
6	OFF	Not used (Always set to OFF)																																																																																																																																																
7	OFF	Not used																																																																																																																																																
8	ON	AP No. 04-15																																																																																																																																																
	OFF	AP No. 20-31																																																																																																																																																

SWITCH NAME	SWITCH NUMBER	SETTING POSITION	FUNCTION	CHECK
SW0 (Dip SW) 	1	ON	For terminating the transmitting side of channels B1 and B2 with 100 Ω. (No.0 Circuit)	
		OFF	To remove the terminating resistor on the transmitting side of channels B1 and B2. (No.0 Circuit)	
	2	ON	For terminating the receiving side of channels B1 and B2 with 100 Ω. (No.0 Circuit)	
		OFF	To remove the terminating resistor on the receiving side of channels B1 and B2. (No.0 Circuit)	
SW1 (Dip SW) 	1	ON	For terminating the transmitting side of channels B1 and B2 with 100 Ω. (No.1 Circuit)	
		OFF	To remove the terminating resistor on the transmitting side of channels B1 and B2. (No.1 Circuit)	
	2	ON	For terminating the receiving side of channels B1 and B2 with 100 Ω. (No.1 Circuit)	
		OFF	To remove the terminating resistor on the receiving side of channels B1 and B2. (No.1 Circuit)	

SWITCH NAME	SWITCH NUMBER	SETTING POSITION	FUNCTION	CHECK
SW2 (Dip SW) 	1		For terminating the transmitting side of channels B1 and B2 with 100 Ω. (No.2 Circuit)	
		OFF	To remove the terminating resistor on the transmitting side of channels B1 and B2. (No.2 Circuit)	
	2		For terminating the receiving side of channels B1 and B2 with 100 Ω. (No.2 Circuit)	
		OFF	To remove the terminating resistor on the receiving side of channels B1 and B2. (No.2 Circuit)	
SW3 (Dip SW) 	1		For terminating the transmitting side of channels B1 and B2 with 100 Ω. (No.3 Circuit)	
		OFF	To remove the terminating resistor on the transmitting side of channels B1 and B2. (No.3 Circuit)	
	2		For terminating the receiving side of channels B1 and B2 with 100 Ω. (No.3 Circuit)	
		OFF	To remove the terminating resistor on the receiving side of channels B1 and B2. (No.3 Circuit)	

The figure in the SWITCH NAME column and the position of  in the SETTING POSITION column indicate the standard setting of the switch. When the switch is not set as shown by the figure and , the setting of the switch varies with the system concerned.

NOTE 1: Set the groove on the switch to the desired position.

NOTE 2: When the power is on, flip the MB switch to ON (UP position) before plugging/unplugging the circuit card.

NOTE 3: The system can receive clock signals from two clock supply routes.

In normal condition, the system synchronizes to the clock signals supplied on the PLO0 of MP card via the Back Wiring Board, and if the clock signals are failed, the clock supply route takes over to PLO1 automatically. Set SW4-1 to SW4-5 as follows.

CONDITIONS	SWITCH	BRT0	BRT1	BRT2	----	BRT23
When one BRT is provided.	SW4-1	ON	/			
	SW4-2	OFF				
	SW4-3	OFF				
	SW4-4	OFF				
	SW4-5	ON				
When more than one BRT is provided.	SW4-1	ON	ON	OFF	----	OFF
	SW4-2	OFF	OFF	OFF		OFF
	SW4-3	OFF	OFF	OFF		OFF
	SW4-4	OFF	OFF	OFF		OFF
	SW4-5	ON	OFF	OFF		OFF

NOTE 4: Mount the BRT card which receives a source clock signals into PIM0.

PROGRAMMING

Please follow programming in document NEC-6809, "NEAX 2000 IPS ISDN SYSTEM MANUAL", BRT Programming Section.

In addition to this programming, the following data needs to be applied,

COMMAND CMAA Y=06 ISDN Type
 (1) 04-15,20-31:AP No of BRT assigned by CM05
 (2) 22: Australian ETSI BRT Initial

Continued,

COMMAND CM35 Y=79 Terminal connection for ISDN BRA
(1) 00-63: B channel Trunk Route No
(2) 0: Point to Point
BRT Initial

Y=144 Layer 1 activation method
(1) 00-63: B channel Trunk Route No
(2) 0: Activated by call event
BRT Initial

Note: Only one BRT initialisation is required at completion of all BRT programming.